



JPW

**PATENT APPLICATION**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Masakazu ISOMURA

Group Art Unit: 2111

Application No.: 10/801,547

Examiner: CHAN

Filed: March 17, 2004

Docket No.: 119102

For: VECTOR PROCESSOR AND REGISTER ADDRESSING METHOD

**SUMMARY OF SUBSTANCE OF TELEPHONE INTERVIEW**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicant's representatives conducted a telephone interview in connection with the above-identified application with Examiner Chan on August 17, 2006. Applicant gratefully acknowledges the courtesies extended to Applicant's representatives by Examiner Chan during that interview. Applicant's separate summary of the telephone interview is contained in the following remarks.

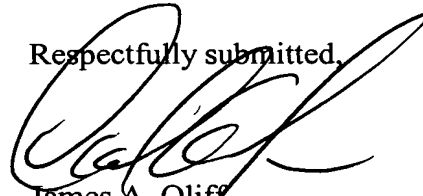
In the telephone interview, Applicant's representatives attempted to clarify the features recited in the pending claims at the Examiner's request. Applicant's representatives summarized how the recited claim features are not disclosed or suggested in the applied prior art cited in the May 19, 2006 Office Action. In that Office Action, claims 1-11 are rejected under 35 U.S.C. §102(e) over U.S. Patent Application Publication No. 2004/0073773 to Demjanenko, under 35 U.S.C. §102(b) over U.S. Patent No. 6,189,094 to Hinds et al. ("Hinds") and under 35 U.S.C. §102(b) over U.S. Patent Application Publication No. 2004/0073773 to Izumisawa et al. ("Izumisawa").

During the telephone interview, Examiner Chan and Applicant's representatives discussed the operation of a vector register in which element registers or scalar registers are circularly specified and how operation of such a circular vector register is not disclosed by the applied prior art references. Specifically, Applicant's representatives argued that a repetitive or iterative set of functions within a linear vector register, as disclosed, for example, by Hinds in Fig. 5, may constitute a repetitive set of functions within a linear register by returning to the same element register starting point. Such an operation does not, however, correspond to operation of a vector register in which the element registers or scalar registers are circularly specified and in which any of the element registers or scalar registers may be specified as the top.

Examiner Chan agreed to consider Applicant's July 19 Amendment in light of the clarification provided during the telephone interview. The Examiner indicated that he would further review the applied prior art references before taking further action regarding this application.

Should any questions arise regarding this communication, all inquiries may be directed to Applicant's undersigned representative at the telephone number set forth below.

Respectfully submitted,



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JAO/PWO

Date: August 22, 2006

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